

The diagram illustrates a multi-processor system architecture. A central hub, labeled '1', is connected to 12 peripheral nodes, labeled '2' through '13'. Each node is represented by a computer icon (monitor, keyboard, and system unit). The connections are labeled with VPI and VCI identifiers:

- Node 2: VPI₁, VCI₁
- Node 3: VPI₁, VCI₁
- Node 4: VPI₁, VCI₁
- Node 5: VPI₁, VCI₃
- Node 6: VPI₁, VCI₂
- Node 7: VPI₁, VCI₃
- Node 8: VPI₁, VCI₃
- Node 9: VPI₁, VCI₁
- Node 10: VPI₁, VCI₂
- Node 11: VPI₁, VCI₂
- Node 12: VPI₁, VCI₁
- Node 13: VPI₁, VCI₁

VPI1	VCI1	Data
VPI1	VCI2	Data